

Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>O I P E</i> <small>several sheets if necessary)</small> <small>AUG 05 2003</small>					Docket Number 204552016500	Application Number 09/412,328		
					Applicant		Yuichi SATO	
					Filing Date October 5, 1999	Group Art Unit 2814		
					Mailing Date August 5, 2003			
U.S. PATENT DOCUMENTS								
Examiner Initials	Ref. No.	Date	Document No.	Name	Class	Subclass	Filing Date If Appropriate	
FOREIGN PATENT DOCUMENTS								
Examiner Initials	Ref. No.	Date	Document No.	Country	Class	Subclass	Translation YES NO	
<i>Dher</i>	1.	3/1979	JP-A-54-037544	Japan			abs.	
	2.	8/1986	JP-A-61-185972	Japan			abs.	
	3.	1/1991	JP3022476	Japan			abs.	
	4.	2/1992	JP-A-04-053090	Japan			abs.	
	5.	6/1995	JP-A-07-161844	Japan			abs.	
	6.	5/1996	JP-A-07-176633	Japan			abs.	
	7.	8/1998	JP-A-10-222985	Japan			abs.	
OTHER DOCUMENTS <small>(including author, title, Date, Pertinent Pages, Etc.)</small>								
Examiner Initials	Ref. No.	Title						
	8.	F. Assaderaghi et al., "Dynamic threshold-voltage MOSFET (DTMOS) for ultra-low voltage VLSI", IEEE Transactions on Electron Devices, vol. 44, no. 3, pp. 414-422, March 1997						
EXAMINER: <i>Douglas A. Willo</i> DATE CONSIDERED: <i>15 Nov 03</i> EXAMINER: Initial if citation considered, whether or not the citation conforms with MPEP 609. Draw a line through the citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.								